KIE2002-LAB 2 SEM 1 SESSION 2026/2027 TIMETABLE

KIE2002 SEM1 2026/2027										
Day/Time	8:00:00 AM	9:00:00 AM	10:00:00 AM	11:00:00 AM	12:00:00 PM	1:00:00 PM	2:00:00 PM	3:00:00 PM	4:00:00 PM	5:00:00 PM
Mon								OCC1		
Tue							OCC2			
Wed							OCC3			
Thu							OCC4			
Frid										

AE1: Design & Simulation of Dipole Antenna using CST
AE2: Design and simulation of a Microstrip Patch Antenna (MPA)
KW7: CONTROL SYSTEM SIMULATION USING MATLAB AND SIMULINK
KW9: CONTROL SYSTEM PERFORMANCE AND STABILITY
KW14: LINE FOLLOWING WITH ArmPi Pro
CM8:ANALOG MODULATION USING MATLAB
CM9: DIGITAL COMMUNICATION 2
DSP1: DSP with MATLAB
DSP2:IMAGE PROCESSING
ED1: Engineering Drawing 1

KIE2002 OCC1 PARALLEL LAB ROTATION

A cohort | Monday, 3:00-5:00 p.m. | Complete by Week 12 | KW14: Control Lab, Block Y, Level G; at least 2 groups in every sequence-eligible week (Weeks 5-12); maximum 2 groups per cohort session |

	W3 19/10/2026 3:00-5:00 p.m.	W4 26/10/2026 3:00-5:00 p.m.	W5 02/11/2026 3:00-5:00 p.m.	W6 09/11/2026 3:00-5:00 p.m.	W7 16/11/2026 3:00-5:00 p.m.	W8 23/11/2026 3:00-5:00 p.m.	W9 07/12/2026 3:00-5:00 p.m.	W10 14/12/2026 3:00-5:00 p.m.	W11 21/12/2026 3:00-5:00 p.m.	W12 28/12/2026 3:00-5:00 p.m.
AE1 Microprocessor Lab, Block Y, Level 4	A7, A8		A1	A6	A3, A5, A9	A11	A2, A4, A12	A10		
AE2 Microprocessor Lab, Block Y, Level 4		A7, A8			A6	A1, A3, A5	A9, A11	A12		A2, A4, A10
KW7 Microprocessor Lab, Block Y, Level 4	A3, A4, A11	A2, A6, A10	A5, A8	A1, A7, A12		A9				
KW9 Microprocessor Lab, Block Y, Level 4		A3, A4	A10, A11	A8	A1	A2, A7, A12	A6	A5, A9		
KW14 Control Lab, Block Y, Level G			A3, A4	A10, A11	A8		A1, A7	A2	A5, A6	A9, A12
CM8 Microprocessor Lab, Block Y, Level 4	A5, A6, A9	A11	A7, A12	A2, A3	A10			A1, A4	A8	
CM9 Microprocessor Lab, Block Y, Level 4		A9	A6	A5	A7, A11, A12	A10	A3		A1, A2, A4	A8
DSP1 Microprocessor Lab, Block Y, Level 4	A1, A2, A12		A9		A4	A6	A5, A8, A10	A3, A7, A11		
DSP2 Microprocessor Lab, Block Y, Level 4		A1, A12			A2	A4		A6, A8	A3, A9, A10	A5, A7, A11
ED1	A10	A5	A2	A4, A9		A8			A7, A11, A12	A1, A3, A6

KIE1002 OCC1 GROUP PATHWAYS

londay, 3:00-5:00 p.m. | KW14: Control Lab, Block Y, Level G; at least 2 groups in every sequence-eligible week (Weeks 5-12); maximum 2 groups per cohort session

Group	W3 19/10/2026 3:00-5:00 p.m.	W4 26/10/2026 3:00-5:00 p.m.	W5 02/11/2026 3:00-5:00 p.m.	W6 09/11/2026 3:00-5:00 p.m.	W7 16/11/2026 3:00-5:00 p.m.	W8 23/11/2026 3:00-5:00 p.m.	W9 07/12/2026 3:00-5:00 p.m.	W10 14/12/2026 3:00-5:00 p.m.	W11 21/12/2026 3:00-5:00 p.m.	W12 28/12/2026 3:00-5:00 p.m.
A1	DSP1	DSP2	AE1	KW7	KW9	AE2	KW14	CM8	CM9	ED1
A2	DSP1	KW7	ED1	CM8	DSP2	KW9	AE1	KW14	CM9	AE2
A3	KW7	KW9	KW14	CM8	AE1	AE2	CM9	DSP1	DSP2	ED1
A4	KW7	KW9	KW14	ED1	DSP1	DSP2	AE1	CM8	CM9	AE2
A5	CM8	ED1	KW7	CM9	AE1	AE2	DSP1	KW9	KW14	DSP2
A6	CM8	KW7	CM9	AE1	AE2	DSP1	KW9	DSP2	KW14	ED1
A7	AE1	AE2	CM8	KW7	CM9	KW9	KW14	DSP1	ED1	DSP2
A8	AE1	AE2	KW7	KW9	KW14	ED1	DSP1	DSP2	CM8	CM9
A9	CM8	CM9	DSP1	ED1	AE1	KW7	AE2	KW9	DSP2	KW14
A10	ED1	KW7	KW9	KW14	CM8	CM9	DSP1	AE1	DSP2	AE2
A11	KW7	CM8	KW9	KW14	CM9	AE1	AE2	DSP1	ED1	DSP2
A12	DSP1	DSP2	CM8	KW7	CM9	KW9	AE1	AE2	ED1	KW14

KIE2002 OCC2 PARALLEL LAB ROTATION

B cohort | Tuesday, 2:00-4:00 p.m. | Complete by Week 12 | KW14: Control Lab, Block Y, Level G; at least 2 groups in every sequence-eligible week (Weeks 5-12); maximum 2 groups per cohort session |

	W3 20/10/2026 2:00-4:00 p.m.	W4 27/10/2026 2:00-4:00 p.m.	W5 03/11/2026 2:00-4:00 p.m.	W6 10/11/2026 2:00-4:00 p.m.	W7 17/11/2026 2:00-4:00 p.m.	W8 24/11/2026 2:00-4:00 p.m.	W9 08/12/2026 2:00-4:00 p.m.	W10 15/12/2026 2:00-4:00 p.m.	W11 22/12/2026 2:00-4:00 p.m.	W12 29/12/2026 2:00-4:00 p.m.
AE1 **Microprocessor Lab, Block Y, Level 4**	B2, B4, B9	B1, B3, B7	B11	B10	B8	B6	B12	B5, B13, B14		
AE2 **Microprocessor Lab, Block Y, Level 4**		B2, B9	B1, B7			B11	B4, B6	B8	B3, B10, B12	B5, B13, B14
KW7 **Microprocessor Lab, Block Y, Level 4**	B6, B12	B4, B5, B8	B2, B10, B14	B1, B3	B7, B13		B9, B11			
KW9 **Microprocessor Lab, Block Y, Level 4**		B6, B12	B4, B8	B5	B1, B3, B10	B13, B14	B2, B7	B9, B11		
KW14 **Control Lab, Block Y, Level G**				B4, B8	B6, B12	B1, B5	B10, B14	B3, B7	B11, B13	B2, B9
CM8 **Microprocessor Lab, Block Y, Level 4**	B3, B11, B13	B10	B12	B2, B6, B7	B5, B9, B14	B4		B1	B8	
CM9 **Microprocessor Lab, Block Y, Level 4**		B13			B11	B2, B3, B10	B5	B4, B6	B7, B9, B14	B1, B8, B12
DSP1 **Microprocessor Lab, Block Y, Level 4**	B1, B7, B14	B11	B3, B5, B6	B9, B12, B13	B2, B4	B8		B10		
DSP2 **Microprocessor Lab, Block Y, Level 4**				B11, B14		B7, B9, B12	B1, B8, B13	B2	B5, B6	B3, B4, B10
ED1	B5, B8, B10	B14	B9, B13				B3	B12	B1, B2, B4	B6, B7, B11

KIE1002 OCC2 GROUP PATHWAYS

uesday, 2:00-4:00 p.m. | KW14: Control Lab, Block Y, Level G; at least 2 groups in every sequence-eligible week (Weeks 5-12); maximum 2 groups per cohort sessio

Group	W3 20/10/2026 2:00-4:00 p.m.	W4 27/10/2026 2:00-4:00 p.m.	W5 03/11/2026 2:00-4:00 p.m.	W6 10/11/2026 2:00-4:00 p.m.	W7 17/11/2026 2:00-4:00 p.m.	W8 24/11/2026 2:00-4:00 p.m.	W9 08/12/2026 2:00-4:00 p.m.	W10 15/12/2026 2:00-4:00 p.m.	W11 22/12/2026 2:00-4:00 p.m.	W12 29/12/2026 2:00-4:00 p.m.
B1	DSP1	AE1	AE2	KW7	KW9	KW14	DSP2	CM8	ED1	CM9
B2	AE1	AE2	KW7	CM8	DSP1	CM9	KW9	DSP2	ED1	KW14
B3	CM8	AE1	DSP1	KW7	KW9	CM9	ED1	KW14	AE2	DSP2
B4	AE1	KW7	KW9	KW14	DSP1	CM8	AE2	CM9	ED1	DSP2
B5	ED1	KW7	DSP1	KW9	CM8	KW14	CM9	AE1	DSP2	AE2
B6	KW7	KW9	DSP1	CM8	KW14	AE1	AE2	CM9	DSP2	ED1
B7	DSP1	AE1	AE2	CM8	KW7	DSP2	KW9	KW14	CM9	ED1
B8	ED1	KW7	KW9	KW14	AE1	DSP1	DSP2	AE2	CM8	CM9
B9	AE1	AE2	ED1	DSP1	CM8	DSP2	KW7	KW9	CM9	KW14
B10	ED1	CM8	KW7	AE1	KW9	CM9	KW14	DSP1	AE2	DSP2
B11	CM8	DSP1	AE1	DSP2	CM9	AE2	KW7	KW9	KW14	ED1
B12	KW7	KW9	CM8	DSP1	KW14	DSP2	AE1	ED1	AE2	CM9
B13	CM8	CM9	ED1	DSP1	KW7	KW9	DSP2	AE1	KW14	AE2
B14	DSP1	ED1	KW7	DSP2	CM8	KW9	KW14	AE1	CM9	AE2

KIE2002 OCC3 PARALLEL LAB ROTATION

C cohort | Wednesday, 2:00-4:00 p.m. | Complete by Week 12 | KW14: Control Lab, Block Y, Level G; at least 2 groups in every sequence-eligible week (Weeks 5-12); maximum 2 groups per cohort

	W3 21/10/2026 3:00-5:00 p.m.	W4 28/10/2026 3:00-5:00 p.m.	W5 04/11/2026 3:00-5:00 p.m.	W6 11/11/2026 3:00-5:00 p.m.	W7 18/11/2026 3:00-5:00 p.m.	W8 25/11/2026 3:00-5:00 p.m.	W9 09/12/2026 3:00-5:00 p.m.	W10 16/12/2026 3:00-5:00 p.m.	W11 23/12/2026 3:00-5:00 p.m.	W12 30/12/2026 3:00-5:00 p.m.
AE1 Microprocessor Lab, Block Y, Level 4	C2, C6, C10	C1, C8, C12	C3, C4			C9	C7	C5	C11	
AE2 Microprocessor Lab, Block Y, Level 4				C2	C1, C3, C10	C8	C9, C12	C4, C6, C7		C5, C11
KW7 Microprocessor Lab, Block Y, Level 4	C4, C9, C12	C5, C6, C7	C1, C10, C11		C8	C2	C3			
KW9 Microprocessor Lab, Block Y, Level 4		C4, C9	C5, C7, C12	C6, C11		C1	C10	C3, C8	C2	
KW14 Control Lab, Block Y, Level G				C5, C9	C4, C7	C11	C1, C6	C10, C12	C3	C2, C8
CM8 Microprocessor Lab, Block Y, Level 4	C1, C7, C11	C3		C4, C8, C12	C2, C5, C9	C6			C10	
CM9 Microprocessor Lab, Block Y, Level 4		C11				C3, C5, C7	C2, C8	C9	C1, C4, C12	C6, C10
DSP1 Microprocessor Lab, Block Y, Level 4		C2	C6, C8, C9	C3, C7, C10	C11, C12	C4	C5	C1		
DSP2 Microprocessor Lab, Block Y, Level 4					C6	C10, C12	C4, C11	C2	C5, C8, C9	C1, C3, C7
ED1	C3, C5, C8	C10	C2	C1				C11	C6, C7	C4, C9, C12

OCC3 GROUP PATHWAYS

dnesday, 2:00-4:00 p.m. | KW14: Control Lab, Block Y, Level G; at least 2 groups in every sequence-eligible week (Weeks 5-12); maximum 2 groups per cohort sess

Group	W3 21/10/2026 3:00-5:00 p.m.	W4 28/10/2026 3:00-5:00 p.m.	W5 04/11/2026 3:00-5:00 p.m.	W6 11/11/2026 3:00-5:00 p.m.	W7 18/11/2026 3:00-5:00 p.m.	W8 25/11/2026 3:00-5:00 p.m.	W9 09/12/2026 3:00-5:00 p.m.	W10 16/12/2026 3:00-5:00 p.m.	W11 23/12/2026 3:00-5:00 p.m.	W12 30/12/2026 3:00-5:00 p.m.
C1	CM8	AE1	KW7	ED1	AE2	KW9	KW14	DSP1	CM9	DSP2
C2	AE1	DSP1	ED1	AE2	CM8	KW7	CM9	DSP2	KW9	KW14
C3	ED1	CM8	AE1	DSP1	AE2	CM9	KW7	KW9	KW14	DSP2
C4	KW7	KW9	AE1	CM8	KW14	DSP1	DSP2	AE2	CM9	ED1
C5	ED1	KW7	KW9	KW14	CM8	CM9	DSP1	AE1	DSP2	AE2
C6	AE1	KW7	DSP1	KW9	DSP2	CM8	KW14	AE2	ED1	CM9
C7	CM8	KW7	KW9	DSP1	KW14	CM9	AE1	AE2	ED1	DSP2
C8	ED1	AE1	DSP1	CM8	KW7	AE2	CM9	KW9	DSP2	KW14
C9	KW7	KW9	DSP1	KW14	CM8	AE1	AE2	CM9	DSP2	ED1
C10	AE1	ED1	KW7	DSP1	AE2	DSP2	KW9	KW14	CM8	CM9
C11	CM8	CM9	KW7	KW9	DSP1	KW14	DSP2	ED1	AE1	AE2
C12	KW7	AE1	KW9	CM8	DSP1	DSP2	AE2	KW14	CM9	ED1

KIE2002 OCC4 PARALLEL LAB ROTATION

D cohort | Thursday, 2:00-4:00 p.m. | Complete by Week 12 | KW14: Control Lab, Block Y, Level G; at least 2 groups in every sequence-eligible week (Weeks 5-12); maximum 2 groups per cohort session |

	W3 22/10/2026 2:00-4:00 p.m.	W4 29/10/2026 2:00-4:00 p.m.	W5 05/11/2026 2:00-4:00 p.m.	W6 12/11/2026 2:00-4:00 p.m.	W7 19/11/2026 2:00-4:00 p.m.	W8 26/11/2026 2:00-4:00 p.m.	W9 10/12/2026 2:00-4:00 p.m.	W10 17/12/2026 2:00-4:00 p.m.	W11 24/12/2026 2:00-4:00 p.m.	W12 31/12/2026 2:00-4:00 p.m.
X3 Electronics Lab, Block Y, Level 2	**D3, D4, D5, D9**	**D1**	**D2**	**D6, D8**		**D7**				
AE1 Microprocessor Lab, Block Y, Level 4			D1							
AE2 Microprocessor Lab, Block Y, Level 4					D1					
KW7 Microprocessor Lab, Block Y, Level 4	D1									
KW9 Microprocessor Lab, Block Y, Level 4		D1								
KW14 Control Lab, Block Y, Level G										D1
CM8 Microprocessor Lab, Block Y, Level 4				D1						
CM9 Microprocessor Lab, Block Y, Level 4							D1			
DSP1 Microprocessor Lab, Block Y, Level 4						D1				
DSP2 Microprocessor Lab, Block Y, Level 4								D1		
ED1									D1	

OCC4 GROUP PATHWAYS

D cohort | Thursday, 2:00-4:00 p.m. | KW14: Control Lab, Block Y, Level G; at least 2 groups in every sequence-eligible week (Weeks 5-12); maximum 2 groups per cohort session | Activity locations and weekly footnotes included

Group	W3 22/10/2026 2:00-4:00 p.m.	W4 29/10/2026 2:00-4:00 p.m.	W5 05/11/2026 2:00-4:00 p.m.	W6 12/11/2026 2:00-4:00 p.m.	W7 19/11/2026 2:00-4:00 p.m.	W8 26/11/2026 2:00-4:00 p.m.	W9 10/12/2026 2:00-4:00 p.m.	W10 17/12/2026 2:00-4:00 p.m.	W11 24/12/2026 2:00-4:00 p.m.
D1	KW7	KW9	AE1	CM8	AE2	DSP1	CM9	DSP2	ED1